



Advance trimming strategy on analog high-precision circuit applications

High-precision circuit applications are becoming more and more demanding because of the increased accuracy in today's IoT, medical, automotive, AI and many more applications. Advanced technology nodes enable very complex and powerful systems, but the increased performance variation induced by process statistics cannot be reduced only at design level.



Most of today's trimming methodologies are based on Monte Carlo Analysis, custom scripts are needed to setup the right sequence of multiple simulations. You will learn:

- How current trimming strategies are performed;
- Which are the main advantages/disadvantages of current approaches;
- Try to define new trimming/calibration verification strategies



If you are a student of Engineering Faculty with:

- Basic knowledge of statistics and variability in IC development
- Basic knowledge of fundamental topologies like OPA, Bandgap etc.
- Knowledge of programming language C like