



UNIVERSITÀ
DEGLI STUDI
DI PALERMO

DIPARTIMENTO DI INGEGNERIA – UNIPA
VIALE DELLE SCIENZE (EDIF. 9)

4° Short Course on VLSI Design

Non-Volatile-Memories: From Physic Principles to VLSI Design (an Industrial Perspective)

When it will happen :
15:00 – 19:00 of the following days:

Week	Meeting
WK1	Tuesday, March 12 th
	Wednesday, March 13 th
WK2	Tuesday, March 26 th
	Wednesday, March 27 th
WK3	Tuesday, April 9 th
	Wednesday, April 10 th

6 Meetings for a total of 24
teaching/exercises hours

Teaching Expert:

Ing. Francesco La Rosa

*CR&D Technologies and eNVM IPs Technical Director
Microcontrollers and Digital ICs Group (MDG)
Member of Technical Staff - Fellow
STMicroelectronics –Rousset (FR)*

Main Topics:

- Floating Gate NVM:
Physics Main Concepts and related Architecture
- Flash NOR memory cell
- Flash NOR memory device internal Architecture
- VLSI Design of low & high voltage analog sub-systems for a Flash NOR memory device

Moderator: **Prof. Costantino Giaconia**
Coordinator: **Prof. Alessandro Busacca**



Dipartimento
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